

## Electronic Acknowledgement Receipt

EFS ID:	2560478
Application Number:	10623559
International Application Number:	
Confirmation Number:	6040
Title of Invention:	Semiconductor apparatus having logic level decision circuit and inter-semiconductor apparatus signal transmission system
First Named Inventor/Applicant Name:	Yoshihisa Iwata
Customer Number:	22850
Filer:	Marvin Jay Spivak/Abebech Hailemariam
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	240522US2S
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Application Type:	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part/.zip	Pages (if appl.)
1		240522usAmendment.pdf	641927 5e0992171deac0d4499e256a0ef89fb7a 725af0	yes	8

Multipart Description/PDF files in .zip description			
Document Description	Start	End	
Miscellaneous Incoming Letter	1	1	
Amendment - After Non-Final Rejection	2	2	
Claims	3	7	
Applicant Arguments/Remarks Made in an Amendment	8	8	

**Warnings:**

**Information:**

Total Files Size (in bytes):	641927
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**New Applications Under 35 U.S.C. 111**  
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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